51.   (New Claim) The device of claim 36 further comprising:

a fifth set of one or more instructions, the fifth set of one or more instructions

when executed by the processor updating memory address within the

reusable executable image.

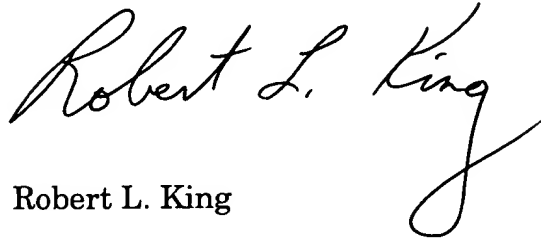52.   (New Claim) The device of claim 46 further comprising:

a sixth set of one or more instructions, the sixth set of one or more

instructions when executed by the processor updating memory

addresses within the pre-internalized image of the selected

program file, wherein execution of the fifth set of one or more

instructions and the sixth set of one or more instructions

corresponds to the pre-internalization mode of operation.

REMARKS

Applicants respectfully request the amendment of claims 35, 36 and 46 and
the addition of two new dependent claims 51 and 52. Applicants now request
consideration of the pending claims 1-52. The additional fees of $36.00 are a result
of the addition of two claims in excess of twenty, and the fees are authorized to be
charged to Applicants' Deposit Account No. 13-4773. No amendment made was
related to the statutory requirements of patentability unless expressly stated
herein. No amendment made was for the purpose of narrowing the scope of any
claim, unless Applicant has argued herein that such amendment was made to
distinguish over a particular reference or combination of references. Please contact

me if there are any issues regarding this communication or the current Application.

Respectfully submitted,

Robert L. King

Robert L. King

Attorney for Applicants

Registration No. 30,185

Telephone No. (512) 996-6839

Facsimile No. (512) 996-6853

35. (Amended) The device of claim 27 further comprising a second memory either contained within the device or external to the device and wherein one of the first memory or the second memory comprises a fourth [plurality of] set of one or more instructions, the fourth set of one or more instructions updating a memory address within the reusable executable image when executed by the processor.

36. (Amended) The device of claim 27 further comprising a second memory either contained within the device or external to the device and wherein one of the first memory or the second memory comprises:

a fourth set of one or more instructions, the fourth set of one or more instructions when executed by the processor moving the reusable executable image to a different location [within the second memory or transferring the pre-internalized image of the selected program file from a second memory; and

a fifth set of one or more instructions, the fifth set of one or more instructions when executed by the processor updating memory address within the reusable executable image].

46. (Amended) The device of claim 44, wherein the virtual machine further comprises:

a fifth set of one or more instructions, the fifth set of one or more instructions when executed by the processor transferring the pre-internalized image of the selected program file from a second memory[; and

a sixth set of one or more instructions, the sixth set of one or more

        instructions when executed by the processor updating memory

        addresses within the pre-internalized image of the selected

        program file, wherein execution of the fifth set of one or more

        instructions and the sixth set of one or more instructions

        corresponds to the pre-internalization mode of operation].

51.   (New Claim) <u>The device of claim 36 further comprising:</u>

        <u>a fifth set of one or more instructions, the fifth set of one or more instructions</u>

        <u>when executed by the processor updating memory address within the</u>

        <u>reusable executable image.</u>

52.   (New Claim) <u>The device of claim 46 further comprising:</u>

        <u>a sixth set of one or more instructions, the sixth set of one or more</u>

        <u>instructions when executed by the processor updating memory</u>

        <u>addresses within the pre-internalized image of the selected</u>

        <u>program file, wherein execution of the fifth set of one or more</u>

        <u>instructions and the sixth set of one or more instructions</u>

        <u>corresponds to the pre-internalization mode of operation.</u>